



Attorney Docket No. 20040144.ORI

Client Docket No. CFP00353

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re App : Herng-Jer Lee et al. : Examiner James C. Kerveros
Serial No. : 10/827,507 : Art Unit 2138
Filed : April 19, 2004 : Confirmation No. 3480
For : Method of Scan Chain Reordering for Lowering VLSI Power Consumption

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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C.F.R. 1.8a).

Alan D. Kamrath

DRAWING AMENDMENT

Dear Sir:

In continuation of the AMENDMENT mailed on September 18, 2006, please amend the above application as follows:

IN THE DRAWINGS

Permission is hereby requested to delete reference numbers "201" and "211" in Figure 2 and reference numbers "401" and "409" in Figure 4 according to the attached formal drawings.

Respectfully submitted,

Herng-Jer Lee et al.

Alan D. Kamrath

Dated: October 16, 2006.

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